

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L24	1	(reference and (modulation modulated) and ramp and circuit and (current adj source) and switch and (capacitor capacitance condenser) and detector and voltage).clm.	US-PGPUB	OR	ON	2006/08/21 15:51
L25	8	(reference and (modulation modulated) and ramp and circuit and (current adj source) and switch and (capacitor capacitance condenser) and voltage).clm.	US-PGPUB	OR	ON	2006/08/21 15:54
L26	2	(reference and (modulation modulated) and (ramp adj circuit) and (current adj source) and switch and (capacitor capacitance condenser) and voltage).clm.	US-PGPUB	OR	ON	2006/08/21 15:57
L28	2	(reference and (modulation modulated) and (ramp adj circuit) and (current adj source) and switch and (capacitor capacitance condenser) and voltage and (charge charged)).clm.	US-PGPUB	OR	ON	2006/08/21 15:58

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	827	307/106.ccls.	US-PGPUB; USPAT	OR	ON	2006/08/21 14:48
L2	2	307/108.ccls. and @pd>="20060623"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/21 15:13
L3	0	307/109.ccls. and @pd>="20060623"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/21 15:13
L4	2	307/110.ccls. and @pd>="20060623"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/21 15:16
L5	8	363/41.ccls. and @pd>="20060623"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/21 15:17
L6	5	363/127.ccls. and @pd>="20060623"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/21 15:20
L7	1	363/133.ccls. and @pd>="20060623"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/21 15:20

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L8	2	363/147.ccls. and @pd>="20060623"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/21 15:20
L9	0	363/147.ccls. and ((current adj (source supply)) and (capacitor capacitance condenser) and (switch transistor) and (voltage adj (detect\$3 sens\$3))) and @pd>="20060623"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/21 15:22
L10	136	((current adj (source supply)) and (capacitor capacitance condenser) and (switch transistor) and (voltage adj (detect\$3 sens\$3))) and @pd>="20060623"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/21 15:35
L11	7	((current adj (source supply)) and (capacitor capacitance condenser) and (switch transistor) and (voltage adj (detect\$3 sens\$3))) and (pulse\$width adj modulat\$4) and @pd>="20060623"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/21 15:36
L12	0	((current adj (source supply)) and (capacitor capacitance condenser) and (switch transistor) and (voltage adj (detect\$3 sens\$3))) and (pulse\$width adj modulat\$4) and (pulse\$width adj modulat\$4).ti. and @pd>="20060623"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/21 15:37
L13	0	(pulse with generator) and (constant adj current adj (source supply)) and (modulated adj current adj (source supply)) and @pd>="20060623"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/21 15:38
L14	1	steele-andrew.in. and @pd>="20060623"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/21 15:39

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L15	59	steele.in. and @pd>="20060623"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/21 15:41
L16	0	(modulated adj current) same (ramp adj circuit) and @pd>="20060623"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/21 15:42
L17	0	(modulated adu current) same (constant adj current) same (pulse\$width adj modulator) and @pd>="20060623"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/21 15:42
L18	2	(modulated adu current) and (constant adj current) and (pulse\$width adj modulator) and @pd>="20060623"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/21 15:43
L19	17	(dc\$dc adj converter) same ((signal adj generator) or (pulse\$width adj modulat\$4)) and @pd>="20060623"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/21 15:44
L20	1	(dc\$dc adj converter) same ((signal adj generator) or (pulse\$width adj modulat\$4)) and (second adj (switch transistor \$fet)) and @pd>="20060623"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/21 15:45
L21	0	(dc\$dc adj converter) same ((signal adj generator) or (pulse\$width adj modulat\$4)) and (second adj (switch transistor \$fet)) and inverter and @pd>="20060623"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/21 15:46

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L22	8	(dc\$dc adj converter) same ((signal adj generator) or (pulse\$width adj modul\$4)) and inverter and @pd>="20060623"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/21 15:46
L23	3	(dc\$dc adj converter) same ((signal adj generator) or (pulse\$width adj modul\$4)) and (push\$pull) and @pd>="20060623"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/21 15:46